

Abstracts

A Broadband Upconverter IC (1992 Vol. I [MWSYM])

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This paper describes the design and performance of a single-chip upconverter IC consisting of a doubly balanced dual-gate FET mixer, a four-stage LO amplifier, and a single-stage IF amplifier. It converts a dc-to-5 GHz input to an 8-to-10 GHz IF. State-of-the-art performance was achieved through the use of advanced models and circuit-design techniques.

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